

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER:

U.S. **UTILITY** Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/888607	CONT/PRIOR F	CLASS 702 714	SUBCLASS 42	ART UNIT 2857 2164 2114	EXAMINER BEAUSSEUL
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Robert Abbott

Method of testing embedded memory array and embedded memory controller for use therewith

PTO-204
12/89

ISSUING CLASSIFICATION															
ORIGINAL				CROSS REFERENCE(S)											
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)										
INTERNATIONAL CLASSIFICATION															

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED			
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	5 _____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED 			
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ _____ _____ (Primary Examiner) (Date)		ISSUE FEE <table border="1"> <tr> <td>Amount Due</td> <td>Date Paid</td> </tr> </table>		Amount Due	Date Paid
Amount Due	Date Paid					
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ _____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER			
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